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LIST OF DOCUMENTS CITED BY APPLICANT *(Use several sheets if necessary)*	TI-36955	TBD 10782240
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

BV		**"A 14-Bit 80-kHz Sigma-Delta A/D Converter: Modeling, Design, and Performance Evaluation" Norsworthy et al, IEEE Journal of Solid-State Circuits Vol. 24 No. 2, April, 1989, pp 256-266**
BV		**"Delta-Sigma Data Converters Theory, Design and Simulation, Norsworthy et al, IEEE Circuits & Systems Society, pp 98-108** (no date)
BV		**"Effective Dithering of Sigma-Delta Modulators", Norsworthy, IEEE 1992, pp 1304-1307**
BV		**"A 102-dB Spurious-Free DR ΣΔ ADC Using a Dynamic Dither Scheme" Gomez, IEEE Transactions on Circuits & Systems, Vol. 47, June 2000, pp 531-535**

EXAMINER	DATE CONSIDERED
B. K. Yen	7/29/04

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